

METHOD OF DETERMINING A MERGING BIT IN AN OPTICAL STORAGE DEVICE

Abstract

A method of determining a merging bit in an optical storage device. The method includes appending one bit to a fourteen-bit set of data output from an eight-to-fourteen modulator, dividing the fifteen-bit set of data into five local digital-sum-values of three bits, adding the five local digital-sum-values to an initial digital-sum-value to generate a sub-digital-sum-value, and generating the merging bit according to the sub-digital-sum-value.